

2. Addendum to the Stratix IV Device Handbook

SIV54002-1.5

This chapter describes changes to the published version of the *Stratix IV Device Handbook*. All changes from Revision 1.4 of this chapter are now incorporated in the main handbook chapters or in *AN 612: Decision Feedback Equalization in Stratix IV Devices*.

Adaptive Equalization (AEQ)

This AEQ information is now located in the *Dynamic Reconfiguration in Stratix IV Devices* chapter.

Decision Feedback Equalization (DFE)

For more information about the DFE feature, refer to *AN* 612: *Decision Feedback Equalization in Stratix IV Devices*.

Power-On Reset Circuitry

The Power-On Reset Circuitry information is now located in the *Hot Socketing and Power-On Reset in Stratix IV Devices* chapter.

Power-On Reset Specifications



• The Power-On Reset Specification information is now located in the *Hot Socketing and Power-On Reset in Stratix IV Devices* chapter.

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Document Revision History

Table 2–1 lists the revision history for this chapter.

Table 2–1.	Document Re	evision History
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Date	Version	Changes	
February 2011	1.5	 Removed the "Decision Feedback Equalization (DFE)" section now that AN 612: Decision Feedback Equalization in Stratix IV Devices is published. 	
		 Moved the "Adaptive Equalization (AEQ)" sections to the Dynamic Reconfiguration in Stratix IV Devices chapter. 	
		 Moved the "Power-On Reset Circuitry" and "Power-On Reset Specifications" sections to the Hot Socketing and Power-On Reset in Stratix IV Devices chapter. 	
		 Minor text edits. 	
September 2010	1.4	 Added corrections for the Adaptive Equalization (AEQ) section of the Stratix IV Dynamic Reconfiguration chapter. 	
		 Added new information for the Decision Feedback Equalization (DFE) feature. 	
April 2010	1.3	 Added corrections for the "Power-On Reset Circuitry" and "Power-On Reset Specifications" sections to of the Hot Socketing and Power-On Reset in Stratix IV Devices chapter. 	
March 2010	1.2	 Moved the "Power-On Reset Circuitry", "Power-On Reset Specifications", "Correct Power-Up Sequence for Production Devices", and "Correct Power-Up Sequence for Production Devices" sections to the Hot Socketing and Power-On Reset in Stratix IV Devices chapter. 	
		 Moved the "Power-On Reset Circuit" and "JTAG TMS and TDI Pin Pull-Up Resistor Value Specification" sections to the Configuration, Design Security, Remote System Upgrades with Stratix IV Devices chapter. 	
		 Moved the "Summary of OCT Assignments" section to the I/O Features in Stratix IV Devices chapter. 	
February 2010	1.1	 Added the "Power-On Reset Circuitry", "Power-On Reset Specifications", "Correction to POR Signal Pulse Width Delay Times", "Correct Power-Up Sequence for Production Devices", "Power-On Reset Circuit", "Summary of OCT Assignments", and "JTAG TMS and TDI Pin Pull-Up Resistor Value Specification" sections. 	
		 Minor text edits. 	
November 2009	1.0	Stratix IV GX enhanced transceiver data rate specifications in -4 commercial speed grade.	
	1.0	 Initial release. 	