

**Table 1. SPI Interface Pin Descriptions**    *Note (1)*

Signal	Purpose	Description
MOSI	master-out slave-in	Outputs data from the master to the inputs of the slaves.
MISO	master-in slave-out	Inputs data from the master to the outputs of the slaves.
SCLK	master-in slave-out	Transfers data from the slave to the input of the master.
SCLK	SPI clock	Clock driven by the master to the slaves. Used to synchronize the data bits.
SS	slave select	Select signal (active low) is driven by the master and sent to the individual slaves, and is used to select the target slave.

**Note to Table 1:**

(1) SPI clock = host clock / (CLK DIV + 2).