

Product Marking Information for Intel® Agilex™ Devices

Version: 1.0

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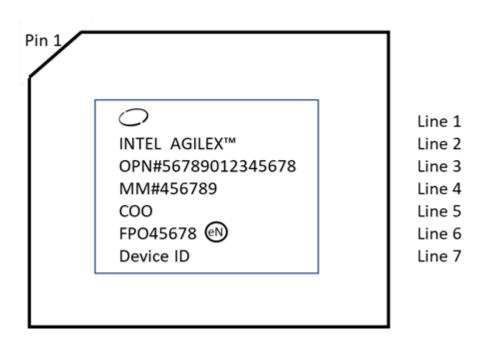


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1.0 Top Mark Layout for Intel Agilex Devices



| Line 1 | Intel swirl logo |
|--------|---|
| Line 2 | Family Name (Intel Agilex) |
| Line 3 | Device Name (OPN) |
| Line 4 | Unit Intel ID (MM) |
| Line 5 | Country of Origin (COO) |
| Line 6 | Lot number (Intel FPO) + 2 nd level interconnect symbol e(N) |
| Line 7 | Unit level ID (2DID) |



2.0 **Document Revision History**

| Date | Version | Changes |
|------------------|---------|------------------|
| December 2021 | 1.0 | Initial release. |