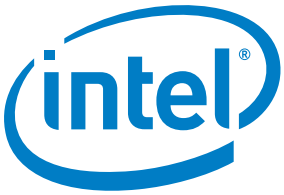


Product Marking Information for Intel® Max® 10 Devices

Version: **2.0**

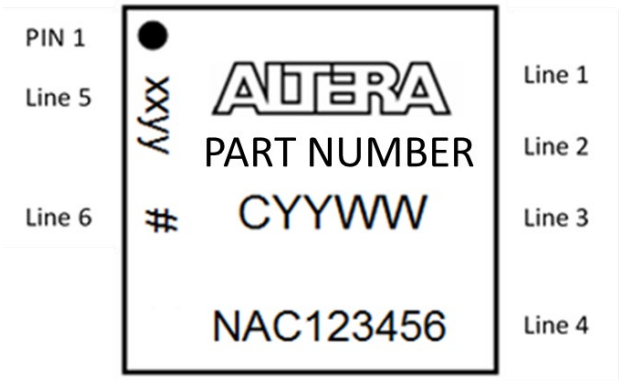
Last updated: **February 2023**

Reference Number: 11G-00016

Contents

1.0 Top Mark Layout for Intel Max 10 Devices

VBGA/YBGA Package



Marking Description	
Line 1	ALTERA® logo
Line 2	Device Name (note 1)
Line 3	Date code (YY = last 2 digits of the year; WW= Workweek)
Line 4	Trace Code
Line 5	Die coordinates in wafer map
Line 6	Bin number
Example	
Line 1	ALTERA®
Line 2	10M08V81G (note 1)
Line 3	C2249
Line 4	S908BP01
Line 5	46-48
Line 6	B

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

Note 1:
Shortened device or part number.
Example: "10M08V81G" for device 10M08SLV81I8G.

EQFP Package

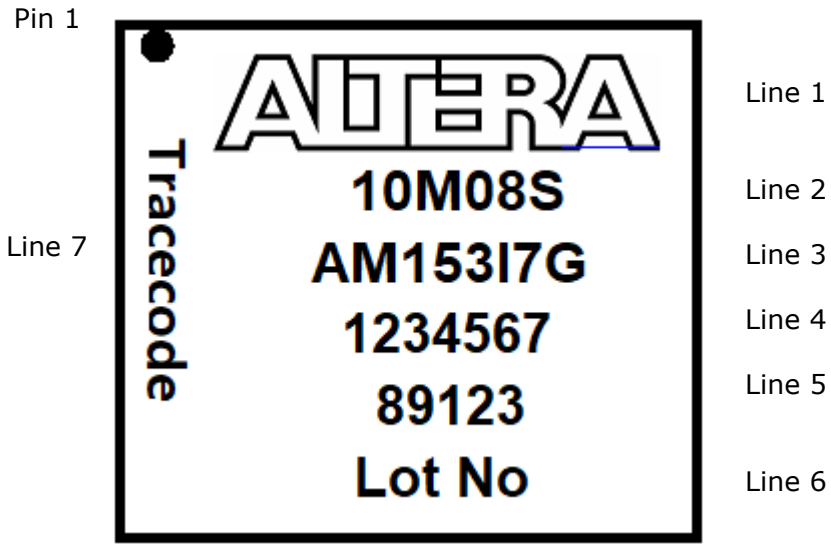
Pin 1

Line 1
Line 2
Line 3
Line 4
Line 5
Line 6

Marking Description	
Line 1	ALTERA® logo
Line 2	Product Family logo
Line 3	Device Name (Example: 10M08SAE144I7G)
Line 4	Datecode
Line 5	Lot Code
Line 6	Trace Code

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

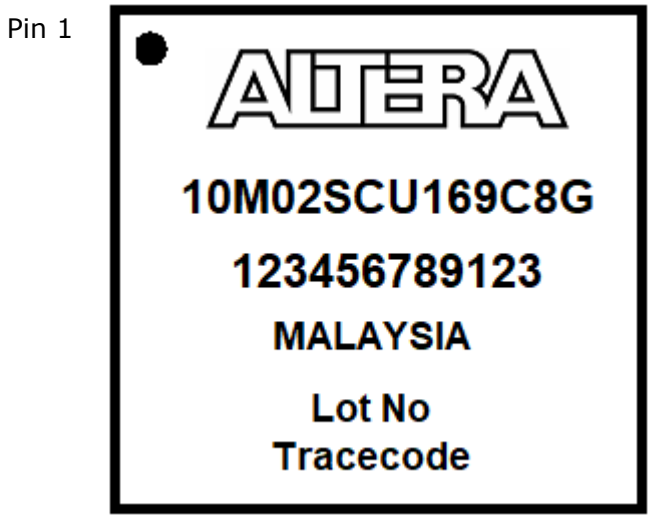
MBGA Package



Marking Description	
Line 1	Company (ALTERA®) logo
Line 2	Device Name (Example: 10M08SAM153I7G)
Line 3	
Line 4	Datecode
Line 5	
Line 6	Lot Code
Line 7	Tracecode

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

UBGA 169 Package



Marking Description	
Line 1	ALTERA® logo
Line 2	Device Name (Example: 10M02SCU169C8G)
Line 3	Datecode
Line 4	Country of Origin
Line 5	Lot Code
Line 6	Trace Code

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

UBGA 324 Package

Marking Description	
Line 1	ALTERA® logo
Line 2	Product Family logo
Line 3	Device Name (Example: 10M16SAU324I7G)
Line 4	Datecode
Line 5	Country of Origin
Line 6	Lot Code
Line 7	Trace Code

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

FBGA Package

Marking Description	
Line 1	ALTERA® logo
Line 2	Product Family logo
Line 3	Device Name (Example: 10M40DCF672I7GAA)
Line 4	Datecode
Line 5	Country of Origin
Line 6	Lot Code
Line 7	Trace Code

6-Week Datecode Scheme	
Work Week	**Date Code**
1 – 6	YY01
7 – 12	YY07
13 – 18	YY13
19 – 24	YY19
25 – 30	YY25
31 – 36	YY31
37 – 42	YY37
43 – 48	YY43
49 – 53	YY49

2.0 Document Revision History

Date	Version	Changes
August 2020	1.0	Initial release.
February 2023	2.0	Updated to add marking lay-outs of the different packages in the product family.