

The Gaudi second-generation AI deep learning mezzanine card, the HL-225B, is designed for massive scale out in data centers. The training processor is built on the high-efficiency architecture of first-generation Gaudi, now in 7nm process technology, to deliver leaps in performance, scalability and power efficiency. The HL-225B Processor complies with US BIS regulations published at www.regulations.gov/document/BIS-2022-0025-0002. Gaudi2 Mezzanine card complies with the OCP OAM 1.1 (Open Compute Platform-Open Accelerator Module) specification, giving customers system design flexibility with choice among products conforming to the spec. The HL-2080 processor features 24 fully programmable 4th generation Tensor Processor Cores (TPCs) natively designed to accelerate a wide array of deep learning workloads, while giving the user the flexibility to optimize and innovate to address to their requirements. It also integrates 96 GB of HBM2E memory and 48 MB SRAM and supports card level TDP of 600 watts.

The Gaudi2 processor offers unmatched scalability of 2.1 Terabits networking capacity with native integration of 21 x 100 GB RoCE v2 RDMA ports to enable inter-Gaudi communication via direct routing.

The Gaudi2 processor integrates dedicated media processor for image and video decoding and pre-processing.



HL-225B Mezzanine Card

PROCESSOR TECHNOLOGY	Gaudi HL-2080
HOST INTERFACE & SCALE-OUT	PCIe Gen 4.0 x 16
MEMORY	96GB HBM2E
TDP	600W
SCALE-UP INTERCONNECT	RDMA (RoCE v2) 21 x 100 Gbps
FORM FACTOR	OCP Accelerator Module V1.1 Compliant



Technology Innovation

GAUDI®2 processor features a unique combination of technology innovations, as a high-performance and fully programmable AI processor with high memory bandwidth/capacity and scale-up based on standard Ethernet technology. Scaling out for multi-node clusters is supported via PCI-e using an external NIC.



Compute Architecture

Based on the proven, shipping training processor architecture, GAUDI2 leverages Habana's fully programmable TPC and GEMM Engine, supporting the most advanced data types for AI: FP8, BF16, FP16, TF32 and FP32. The TPC core was designed to support Deep Learning training and inference workloads. It is a VLIW SIMD vector processor with instruction set and hardware that were tailored to serve these workloads efficiently.



Memory

Memory bandwidth and capacity are as important as compute capability. GAUDI2 incorporates the most advanced HBM memory technology, supporting extremely high memory capacity of 96GB and total throughput of 2.4TB/s. Gaudi's cutting-edge HBM controller is optimized for both random access and linear access, providing record-breaking throughput in all access patterns.



Scale Up with Integrated RDMA

GAUDI® is "the only AI training processor to integrate on-chip RDMA (RoCEv2) to interface with mature and widely used Ethernet networking. The HL-2080 chip interconnect technology is based on 42 pairs of 56Gbps Tx/Rx PAM4 SerDes configured as 21 ports of 100Gb Ethernet.

SynapseAI® Software Suite

Designed to facilitate ease of use and high-performance training on Habana's AI processors, SynapseAI® Software Suite enables efficient mapping of neural network topologies onto Gaudi family of hardware. The software suite includes Habana's graph compiler and runtime, performance optimized TPC kernel library, firmware and drivers, and developer tools such as the TPC programming tool kit for custom kernel development and SynapseAI Profiler. SynapseAI is integrated with popular frameworks, TensorFlow and PyTorch, and optimized for training on Gaudi family of AI processors. Data scientists and developers can migrate their existing models to run on Gaudi2 with minimal code changes. The Habana Developer Site is the hub where developers can find a wealth of information to get started with training on Gaudi AI processors, including tutorials, reference models, how-to guides, documentation and more. It also hosts a Forum for the Habana developer community.



For more details on Gaudi's performance and scaling, see the Habana Gaudi2 Whitepaper.

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