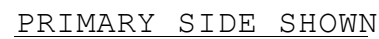



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

1. ASSEMBLY HOUSE TO VISUALLY VERIFY BOARD BEING ASSEMBLED HAS INTEL MFR NUMBER 100-0330705-A1 REV A IN ETCH ON BOTTOM SIDE.
2. BOARDS MUST BE BAKED FOR A DURATION OF 8 HOURS AT 250 DEGREES FARENHEIT IMMEDIATELY PRIOR TO ASSEMBLY.
3. ASSEMBLE BOARD PER IPC-A-610 (LATEST REVISION) STANDARD.
4. ALL MATERIALS USED MUST BE ROHS COMPLIANT.
5. HANDLE MOISTURE SENSITIVE DEVICES (MSD) PER MANUFACTURE'S CALL OUT ON MSD LABEL. IF MSD LABEL IS MISSING OR NOT LEGIBLE THEN ALL PLASTIC PACKAGED COMPONENTS ARE TO BE CONSIDERED MSD LEVEL 6 AND USE EIA/JEP 113-A FOR HANDLING.
6. SQUARE PIN PAD DENOTE PIN 1 OF ICs/CONNECTORS, POSITIVE END FOR CAPACITORS OR CATHODE END FOR DIODES.
7. REWORK MUST BE APPROVED BY CUSTOMER BEFORE BEING ACCEPTABLE USE IPC-R-700 (LATEST REVISION) FOR ALL PCA REWORK APPROVED.
8. INSTALL BAR CODE LABEL APPROX WHERE SHOWN, INCLUDING S/N.
9. VENDOR TO KEEP QUALITY CONTROL INK STAMPINGS OFF SIGNAL TRACES AND PADS EVEN IF COVERED BY SOLDERMASK.



			MATL	DWN				
				PSG Board Design Team				
				CHKR				
			SPEC	Jagadeeswaran				
-	XX-XXX-XXXX			DSGN ENGR	TITLE PCB ASSEMBLY CYCLONE V GT DEVELOPMENT KIT (NON-ENP			
				Yaseen				
DASH	NEXT ASSY	USED ON	FNSH	RELEASE				
APPLICATION				21-FEB-2023				
THIRD ANGLE PROJECTION	TOLERANCES: DEC .X ±							
	UNLESS OTHERWISE SPECIFIED:			DEC .XX ± .01				
	DIMENSIONS ARE IN INCHES IN			DEC .XXX ± .005				
	ACCORDANCE WITH ANSI Y14.5.			DEC .XXXX ± .002				
	DRILL TOLERANCES ARE PER 10387			FRACTIONS ±				
				ANGLES ± 1'				
				CONTRACT NO.				
				XXXXXXX				
					SIZE	CAGE CODE	MFR NUMBER	REV
					D	N/A	100-0330705-A	1A
					SCALE: 1/1			SHEET 1 OF 1