

Intel® Stratix® 10 FPGA- Helloworld Design on Nios® V/m Processor

Date: 6/30/2023

Revision: 1

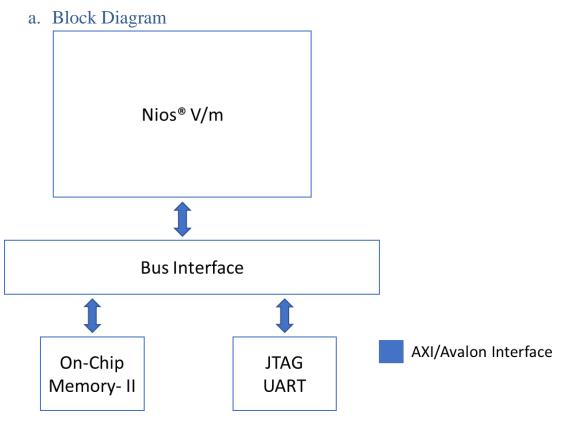
©2017 Intel Corporation. All rights reserved. ALTERA, ARRIA, CYCLONE, HARDCOPY, INTEL, MAX, MEGACORE, NIOS, QUARTUS and STRATIX words and logos are trademarks of Intel Corporation and registered in the U.S. Patent and Trademark Office and in other countries. All other words and logos identified as trademarks or service marks are the property of their respective holders as described at www.altera.com/common/legal.html. Intel warrants performance of its semiconductor products to current specifications in accordance with Intel's standard warranty, but reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.

Contents

1. Theory of Operation	3
a. Block Diagram	
b. IP Cores used	3
2. Executing the Design on Devkit	3
a. Creating the Design	
b. Expected Results	4

1. Theory of Operation

Nios® V/m Processor-based Helloworld example design on the Intel Stratix® 10 FPGA.



b. IP Cores used

The following IPs are used in this design.

- NIOSV/m soft processor core
- On Chip RAM
- JTAG UART

2. Executing the Design on Devkit

a. Creating the Design

Note: Please refer to the readme.txt file in the package for the steps to create the design, application and generate the programing files.

- Unpackage/extract the design in your working directory
- Locate the "ready_to_test" folder within the package
- The folder contains the necessary files for executing the application on the board. Refer to the readme file for the steps to program the application files on the board.
- Validate the design by observing the prints on the terminal

b. Expected Results

The following is the output as observed on the JTAG UART terminal. The output is analogous to the logic from the application code. Users should be able to observe same output on their terminal/setup.

HAL OS:

Hello world, Hello world, Hello world, Hello world, Hello world, Hello world,	this this this this this this this this	is tł is tł is tł is tł is tł	ne Nios ne Nios ne Nios ne Nios ne Nios	V/m V/m V/m V/m	cpu cpu cpu cpu cpu cpu	checking checking checking checking checking	in in in in	1 2 3 4 5
Hello world, Hello world,						checking checking		
Hello world,						checking		
Hello world,						checking		
Hello world,	this :	is tł	ne Nios	V/m	cpu	checking	in	10
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,						checking		
Hello world,								
Hello world,								
Hello world,						-		
Hello world,								
Hello world,	this	is th	ne Nios	V/m	сри	checking	in	30

Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	70
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	71
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	72
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	73
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	74
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	75
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	76
Hello	world,							checking		
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	78
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	79
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	80
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	81
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	82
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	83
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	84
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	85
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	86
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	87
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	88
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	89
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	90
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	91
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	92
Hello	world,							checking		
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	94
Hello	world,							checking		
Hello	world,	this	is	the	Nios	V/m	cpu	checking	in	96
	world,							checking		
Hello	world,							checking		
	-							checking		
Bye wo	-					•				
-										

uCOS-II:

Hello from main... Task1 -- TOS: 0x299f0, BOS: 0x279f4 Task2 -- TOS: 0x2b9f0, BOS: 0x299f4 Task3 -- TOS: 0x2d9f0, BOS: 0x2b9f4 Stat -- TOS: 0x2e868, BOS: 0x2e06c Idle -- TOS: 0x2f068, BOS: 0x2e86c Hello from task1: 0 Hello from task2: 0 Hello from task3: 0 Hello from task3: 1 Hello from task2: 1 Hello from task3: 2 Hello from task1: 1 Hello from task3: 3 Hello from task2: 2 Hello from task3: 4 Hello from task3: 5 Hello from task2: 3 Hello from task1: 2 Hello from task3: 6 Hello from task3: 7 Hello from task2: 4

FreeRTOS:

Hello FreeRTOS from main... Hello from task1: 0 Hello from task2: 0 Hello from task3: 0 Hello from task3: 1 Hello from task2: 1 Hello from task3: 2 Hello from task1: 1 Hello from task3: 3 Hello from task2: 2 Hello from task3: 4 Hello from task2: 3 Hello from task1: 2 Hello from task2: 4 Hello from task1: 3 Hello from task1: 4