# Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 2024.2 Release

# **Release** Notes

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# 1 Introduction

The Intel® CPU Runtime for OpenCL<sup>™</sup> Applications 2024.2 release provides OpenCL<sup>™</sup> support for Intel® CPU devices.

This document contains system requirements, installation instructions, information about known issues and limitations, and legal information.

#### **Customer support**

For technical support, including answers to questions not addressed in the installed product, go to the OpenCL<sup>™</sup> forum at this site: <u>https://software.intel.com/en-us/forums/opencl</u>.

# 2 What's New

- Implemented clGetKernelSuggestedLocalWorkSizeKHR API which is introduced in OpenCL spec 3.0.
- Optimized the private memory management to avoid potential out of resource error.
- Bug fixing for stabilization.

# 3 System Requirements

#### 3.1 Processor Requirements

The Intel® CPU Runtime for OpenCL<sup>™</sup> Applications 2024.2 provides CPU device support on the following processors:

- Intel Core<sup>™</sup> processor family with Intel<sup>®</sup> Streaming SIMD Extensions 4.2 (Intel<sup>®</sup> SSE4.2) support or higher
- Intel Xeon® processor E3, E5, and E7 families with Intel® SSE4.2 support or higher
- Intel Xeon® Scalable processors Platinum, Gold, Silver, Bronze families with Intel® SSE4.2 support or higher

Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 2024.2 provides optimizations for processors that support the following instruction sets:

- Intel® Advanced Vector Extensions 512 (Intel® AVX-512) Foundation instructions (Intel® AVX-512F), Intel® AVX-512 Conflict Detection instructions (AVX-512CD), Intel® AVX-512 Doubleword and Quadword instructions (AVX-512DQ), Intel® AVX-512 Byte and Word instructions (AVX-512BW) and Intel® AVX-512 Vector Length Extensions (AVX-512VL)
- Intel® Advanced Vector Extensions 2 (Intel® AVX2)
- Intel® Advanced Vector Extensions (Intel® AVX)
- Intel® Streaming SIMD Extensions 4.2 (Intel® SSE4.2)
- Intel® Advanced Vector Extensions (Intel® AVX-VNNI)

*NOTE*: Incompatible or proprietary instructions of non-Intel processors may cause the analysis capabilities of this product to function incorrectly. Any attempt to analyze code not supported by Intel processors may lead to failures in this product.

#### 3.2 Software Requirements

For 2024.2 release, the supported Operating Systems are listed below:

- Microsoft Windows\* 10 (IA-32 or Intel® 64)
- Microsoft Windows\* 11 (IA-32 or Intel® 64)
- Microsoft Windows\* Server 2022 (IA-32 or Intel® 64)
- Microsoft Windows\* Server 2019 (IA-32 or Intel® 64)

- Red Hat\* Enterprise Linux\* 8.x, 9.x
- SUSE Linux Enterprise Server (SLES)\* 15 SP3, SP4, SP5
- Ubuntu\* 20.04 LTS, 22.04
- Fedora\* 38, 39

# 4 Installation or Uninstallation

#### 4.1 Microsoft Windows\*

**Note:** If an older version of Intel® CPU Runtime for OpenCL Application is installed, please uninstall it before installing the new version.

**1)** To install the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications on Windows<sup>\*</sup> systems, download and run the Runtime package; follow the installer prompts to install.

**2)** To uninstall the Intel® CPU Runtime for OpenCL<sup>™</sup> Applications, go to Control Panel > Programs and Features > OpenCL<sup>™</sup> Runtime > Uninstall.

#### 4.2 Linux

Please follow the instructions on Install Using Package Managers to setup the repository and install the package "intel-oneapi-runtime-opencl".

#### 5 Known Issues or Limitations

 Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications has a dependency on Intel<sup>®</sup> Threading Building Blocks (Intel<sup>®</sup> TBB) that is included in the Intel<sup>®</sup> CPU Runtime installation.

The Intel® TBB libraries included in the package have different names which are different than previous Intel TBB or the open-source Threading Building Blocks because of breaking changes. This change ensures that no load conflict takes place with pre-installed libraries.

If the OpenCL<sup>TM</sup> host code uses features of previous version of Intel<sup>®</sup> TBB libraries, the application performance may vary (e.g. due to oversubscription issues in case of both standalone library pool and OpenCL thread pool being fully loaded).

2) Configuration file parameter CL\_CONFIG\_CPU\_TARGET\_ARCH cannot be set from cl.cfg. It can only be set as an environment variable.

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