Intel ${ }^{\circledR}$ Processor Graphics for
Pentium ${ }^{\circledR}$ and Celeron ${ }^{\circledR}$
Processors

## Product Guide

November 2012
Revision 002

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## Revision History

| Document <br> Number | Revision <br> Number | Description | Revision Date |
| :---: | :---: | :--- | :--- |
| 325628 | 001 | Initial release. | June 2011 |
| 325628 | 002 | Update for 3 rd Gen HD Graphics | November 2012 |

## 1 I ntroduction

Intel ${ }^{\circledR}$ HD Graphics is the next-generation graphics technology available on the nextgeneration Intel ${ }^{\circledR}$ micro-architecture codenamed Ivy Bridge. Based on the 22 nm process technology with 3D Tri-Gate transistors, it provides improved graphics performance compared to the previous Intel ${ }^{\circledR}$ Celeron ${ }^{\circledR}$ processor-based platforms. It combines cutting-edge 3D graphics, improved HD visuals, advanced video capabilities and enhanced display connectivity all built-in to the processor without the need of a discrete graphics card. It integrates high-performance graphics and media processing on the processor, putting two key embedded application components - CPU processing and graphics - on a single chip.

Intel ${ }^{\circledR}$ HD Graphics

## 2 Intel ${ }^{\circledR}$ HD Graphics

Intel ${ }^{\circledR}$ HD Graphics is graphics built into the CPU, providing superior performance over previous generation Intel ${ }^{\circledR}$ HD Graphics while creating additional headroom for tomorrow's embedded usages. It includes features such as accelerated graphics processing, faster 3D rendering and complex shading for increased realism. It delivers a competitive advantage, ideal to suit the needs of a broad embedded market whether that is in the Retail, Digital Signage, Gaming, Industrial, Military, Aerospace, Government, Digital Security/Surveillance or Medical segments.

Table 1 describes the new features introduced in the next-generation graphics engine as well as those best suited for the embedded computing market. Table 2 describes the existing built-in visual features in the 2 nd Generation Intel ${ }^{\circledR}$ Core ${ }^{\mathrm{m}}$ processor. For a comprehensive list of supported features, refer to the Intel ${ }^{\circledR}$ Graphics Software for Intel ${ }^{\circledR}$ HD Graphics Family Product Requirements Document (PRD). The Iatest Intel ${ }^{\circledR}$ HD Graphics Driver packages are available for download at http://www. intel.com.

Table 1. New Features

| Features | Description and Benefits |
| :--- | :--- |
| Media | HW Accelerated decode for improved media <br> playback for MPEG2, AVC and VC1 |
| Improved MPEG2/AVC/VC1 decode <br> performance | New API support and performance enhancements <br> for improved 3D visuals |
| 3D Graphics | New API support and performance enhancements <br> for improved 3D visuals |
| DirectX* 11 | API support and performance enhancements for <br> improved 3D visuals |
| OpenGL* 3.1 | 3 non-symmetric independent and concurrent <br> display support |
| OpenCL 1.1 |  |
| Display |  |

Table 2. Existing Features

| Features |  |
| :--- | :--- |
| Description and Benefits |  |
| ProcAmp | Adjust hue, brightness, saturation and contrast of <br> video channels independently of other on screen <br> content |
| Display | Dedicated Embedded DisplayPort (eDP) support on <br> low-power CPU's and support for eDP on PCH for <br> low-power and scalable CPU's offers design <br> flexibility |
| Embedded DisplayPort* (eDP) | Increased color depth for improved accuracy and <br> vividness |
| 12bpc Color Depth (DisplayPort* and <br> HDMI) | Impressive power savings allows reduction of core <br> Power Management |
| Render Standby | Praphics voltage down to OV |
| Extended Power Saving State (Power savings technology allowing powering down <br> Reduction During Idle) |  |

### 2.1 Supported Operating Systems

The following operating systems are supported:

- Microsoft* Windows* XP
- Microsoft* Windows* 7 Starter/Home Basic/Home Premium/Professional/Ultimate
- Microsoft* Windows* Embedded Standard 7
- Microsoft* Windows* 8
- Microsoft* Windows* Embedded Standard 2009
- Microsoft* Windows* Embedded POSReady 2009
- Linux* ${ }^{1,2}$


## Notes:

1. Intel does not develop distribution specific drivers for Intel ${ }^{\circledR}$ HD Graphics. All Linux driver components are up-streamed to their respective repositories once a quarter. Information on these components is posted on www.intellinuxgraphics.com under the Downloads section. Distributions may pick up components as they choose.
2. Linux driver features represent only a subset of those supported by the Intel ${ }^{\circledR} \mathrm{HD}$ Graphics driver. For additional information, visit www. intellinuxgraphics.com.

Intel ${ }^{\circledR}$ HD Graphics

### 2.2 Feature Support by Platform

Table 3 represents a subset of the features available on these platforms. Some features are also Operating System dependent. Refer to the Software Graphics Product Requirements Document for full details.

Table 3. Feature Supported by Platform

| $d^{e t}$ | 2010 Intel ${ }^{\circledR}$ Core ${ }^{\text {m }}$ <br> Processor <br> Family with I ntel ${ }^{\circledR} 5$ Series Chipset | Intel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with Intel ${ }^{\circledR} 6$ Series Chipset | I ntel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with Intel ${ }^{\circledR} 7$ Series Chipset |
| :---: | :---: | :---: | :---: |
| Media |  |  |  |
| Hardware Accelerated MPEG2 Decode | Y | Y | Y |
| Hardware Accelerated VC1 Decode | Y | Y | Y |
| Hardware Accelerated AVC/H. 264 Decode | Y | Y | Y |
| Hardware Accelerated Decode (MPEG2, VC1, AVC/H.264) in All Rotated Modes | $\bigcirc$ | Y | Y |
| Multi-Function Decode + Deblocking | $N$ | Y | Y |
| Dual Video Hardware Accelerated Decode | Y | , Y | Y |
| Accelerated MPEG2 Encode | N | N | N |
| Accelerated AVC/H. 264 Encode | N | N | N |
| Advanced Deinterlacing | d $Y$ | Y | Y |
| SD/HD Sharpness Control (Post Processing) | Y | Y | $e^{O} \quad Y$ |
| Adaptive Contrast <br> Enhancement (Post Processing) | N | Y | Y |
| Skin Tone Enhancement (Post Processing) | $N$ | Y | Y |
| Total Color Control (Post Processing) | $\mathrm{N}$ | Y | Y |
| 12bpc Color Depth (DisplayPort* and HDMI) | Y | Y | Y |
| Display/ Audio |  |  |  |
| DisplayPort 1.1a Audio Support (LPCM 2 channel \& AC3) | Y | Y | Y |
| HDMI v1.4 with Stereoscopic 3D | N | N | N |
| Dual Stream Audio | Y | - $Y$ | Y |


| $d^{2+i n} e^{d e n}$ | 2010 Intel ${ }^{\text {® }}$ Core ${ }^{\text {m }}$ <br> Processor <br> Family with Intel ${ }^{\circledR} 5$ Series Chipset | Intel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with I ntel ${ }^{\text {® }} 6$ Series Chipset | Intel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with Intel ${ }^{\circledR} 7$ Series Chipset |
| :---: | :---: | :---: | :---: |
| Embedded DisplayPort* | Y | Y | Y |
| Dual LVDS (Integrated LVDS + SDVO LVDS) | $Y$ | Y | Y |
| EDID-less display support | - $Y$ | Y | Y |
| Wireless Display ${ }^{1}$ | Y | N | N |
| SDVO-DVI Support | Y | Y | Y |
| SDVO-LVDS Support | Y | Y | Y |
| SDVO-HDMI Support | N | N | N |
| SDVO-TVOut Support | Y | N | N |
| SDVO-CRT Support | Y $8^{\circ}$ | Y | Y |
| Support for Analog CRT | $Y$ | Y | Y |
| Support for External Digital Displays (DVI, HDMI, DP) | $\text { O } \quad Y$ | Y | Y |
| Three active displays | N | N | Y |
| 3D Graphics |  |  |  |
| DirectX* 10.0 | Y | Y | Y |
| DirectX* 10.1 | N | \% Y | Y |
| DirectX* 11.0 | N | Y | Y |
| OpenGL* 2.1 | Y | Y | Y ${ }^{8}$ |
| OpenGL* 3.0 | N | Y | Y |
| OpenGL* 3.1 | $\bigcirc$ | N | d $Y$ |
| OpenCL 1.1 | N | N | Y |
| Power Management |  |  |  |
| Intel ${ }^{\circledR}$ Graphics Dynamic Frequency | Y | Y | Y |
| Render Standby | Y | ( $Y$ | Y |
| Intel ${ }^{\circledR}$ Display Refresh Rate Switching - For Seamless and Static Panel Displays | Y | Y | Y |
| Intel ${ }^{\circledR}$ Graphics Performance Modulation Technology | $e^{O}$ | Y | $Y$ |
| Intel ${ }^{\circledR}$ Display Power Savings Technology (DPST) | Y | Y | Y |
| Intel ${ }^{\circledR}$ Smart 2D Display Technology (S2DDT) | Y | $Y$ | Y |
| Intel ${ }^{\circledR}$ Rapid Memory Power | Y | V Y | Y |

Intel ${ }^{\circledR}$ HD Graphics

|  | 2010 Intel ${ }^{\circledR}$ Core ${ }^{\mathrm{mm}}$ Processor Family with Intel ${ }^{\circledR} 5$ Series Chipset | Intel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with I ntel ${ }^{\circledR} 6$ Series Chipset | I ntel ${ }^{\circledR}$ Processor Graphics for Pentium and Celeron with I ntel ${ }^{\circledR} 7$ Series Chipset |
| :---: | :---: | :---: | :---: |
| Management |  |  |  |
| Intel ${ }^{\circledR}$ Automatic Display Brightness (ADB) | $Y$ | Y | Y |
| Extended Power Saving State | , N | Y | Y |

${ }^{1}$ Intel ${ }^{\circledR}$ Wireless Display is not Plan of Record for embedded long life support, requires Intel ${ }^{\circledR}$ My WiFi Technology and is only supported on Mobile products. Contact your local FAE for interest in this feature.

